

TFW 2133

PTO/SB/21 (09-04)

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/932,086	
	Filing Date	17 August 2001	
	First Named Inventor	Georg Farkas	
	Art Unit	2133	
	Examiner Name	Guy J. Lamarre	
Total Number of Pages in This Submission	6	Attorney Docket Number	CH 000018

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	North Weber & Baugh LLP		
Signature			
Printed name	Michael V. North		
Date	November 16, 2005	Reg. No.	46,963

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/932,086 Confirmation No.: 5457  
Applicant(s) : Georg Farkas  
Filing Date : 17 August 2001  
Title : Arrangement for Testing Integrated Circuits  
Group Art Unit : 2133  
Examiner : Guy J. Lamarre  
Docket No. : CH 000018

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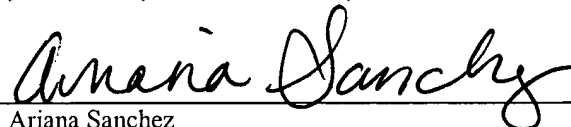
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Dated: 16 November 2005

By:

  
Ariana Sanchez

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**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT**

SIR:

This paper is responsive to the Notice of Non-Compliant Amendment mailed November 3, 2005, having a shortened statutory response period expiring on December 3, 2005. Further examination and reconsideration are respectfully requested in view of the modified claim status identifiers, and Response to Office Action on July 28, 2005, which is herein incorporated by reference.

The Applicant respectfully points out that the previous claim status identifiers are acceptable under the U.S. Patent and Trademark Office OF Notice, dated 05 July 2005 in which additional acceptable claim status identifiers are set forth.

Appl. No. : 09/932,086  
Atty. Docket No. : CH 000018  
Response Date : 16 November 2005

Properly identified **Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this document.